



A Designer's Guide to Asynchronous VLSI

By Peter A. Beerel, Recep O. Ozdag, Marcos Ferretti

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Bypass the limitations of synchronous design and create low power, higher performance circuits with shorter design times using this practical guide to asynchronous design. The fundamentals of asynchronous design are covered, as is a large variety of design styles, while the emphasis throughout is on practical techniques and real-world applications.

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Editorial Review

About the Author

Peter A. Beerel is CEO of TimeLess Design Automation - his own company commercializing asynchronous VLSI tools and libraries - and an Associate Professor in the Electrical Engineering Department at the University of Southern California (USC). Dr Beerel has 15 years experience of research and teaching in asynchronous VLSI and has received numerous awards including the VSoE Outstanding Teaching Award in 1997 and the 2008 IEEE Region 6 Outstanding Engineer Award for significantly advancing the application of asynchronous circuits to modern VLSI chips.

Recep O. Ozdag is IC Design Manager at Fulcrum Microsystems and a part-time Lecturer at USC, where he received his Ph.D. in 2004. He is a recipient of the British Chevening Scholarship, the Turkish Ministry of Education Post-Graduate Scholarship and the Turkish Higher Education Council Scholarship.

Marcos Ferretti is one of the founders of PST Eletronica S. A. (Positron), Brazil - an automotive electronic systems manufacturing company, where he is currently Vice President of Technology. He received his Ph.D. from the University of Southern California (USC) in 2004 and was co-recipient of the USC Electrical Engineering-Systems Best Paper Award in the same year.

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